

Electronic Acknowledgement Receipt

EFS ID:	1464749
Application Number:	10702437
International Application Number:	
Confirmation Number:	9385
Title of Invention:	Substrate-biased silicon diode for electrostatic discharge protection and fabrication method
First Named Inventor/Applicant Name:	Chyh-Yih Chang
Customer Number:	570
Filer:	Clark A. Jablon/Megumi McKeever
Filer Authorized By:	Clark A. Jablon
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Submitted with Payment	yes
Payment was successfully received in RAM	\$900
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part/.zip	Pages (if appl.)
1	Extension of Time	681954_166U2_Extention.pdf	519964	no	1
Warnings:					
Information:					
2	Fee Worksheet (PTO-06)	fee-info.pdf	8193	no	2
Warnings:					
Information:					
Total Files Size (in bytes):					528157
<p>This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.</p> <p>New Applications Under 35 U.S.C. 111 If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.</p> <p>National Stage of an International Application under 35 U.S.C. 371 If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.</p>					